

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
ERRATICO

Serial No. 09/899,573

Confirmation No. 1615

Filing Date: July 5, 2001

For: INTEGRATED STRUCTURE

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)  
) Examiner J. Mondt  
) Art Unit: 2826  
) Fax No.: 703-308-7722  
)  
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#9  
Election

RESPONSE TO REQUIREMENT FOR RESTRICTION

Director, U.S. Patent and Trademark Office  
Washington, D.C. 20231

Sir:

In response to the restriction requirement of March 12, 2002, Applicant hereby elects Group I, Claims 12-26, drawn to a semiconductor integrated circuit, classified in class 257, subclass 510. It is respectfully requested that the Examiner use his discretion under 35 U.S.C. §121 and withdraw the restriction requirement since search and examination of the entire application can be made without serious burden.

If any extension and/or fee is required, authorization is given to charge Deposit Account No. 01-0484.

Respectfully submitted,



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In re Patent Application of:

**ERRATICO**

Serial No. 09/899,573

Filed: 7/5/01

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**CERTIFICATE OF FACSIMILE TRANSMISSION**

I HEREBY CERTIFY that the foregoing correspondence has been forwarded via facsimile number 703-305-7687 to the Director, U.S. Patent and Trademark Office, Washington, D.C. 20231 this 8<sup>th</sup> day of April, 2002.

  

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**TELECOPY COVER SHEET**

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**TO: EXAMINER J. MONDT (U.S. PATENT AND TRADEMARK OFFICE)****CLIENT NUMBER: 53292****TELEPHONE: 703-306-0531****FAX: 703-308-7722****FROM: CHRISTOPHER F. REGAN, ESQ.****DATE: January 11, 2002****NUMBER OF PAGES 3****COMMENTS/INSTRUCTIONS:****Re: U.S. Patent Application Serial No. 09/899,573****Attached is Applicant's Response to Requirement for Restriction**

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